REMARKS

Claims 1-23 are pending in the application. Claims 1-9, 14, 16, and 17-23 are rejected. Claims 10-13 and 15 are objected to. Claims 1, 3, 8, 11, 12, 14, 17, and 18 have been amended. Claims 2, 4, 9, 10, 16, and 19 have been canceled.

The claims 9 and 16-17 were rejected under 35 USC 112, second paragraph. Claims 9 and 16 have been canceled. Claim 17 has been amended to depend from claim 15. Therefore, the claims are now believed to be allowable under 35 USC 112, second paragraph.

Claims 1-7 and 18-23 were rejected under 35 USC 103(a) as being unpatentable over Kim in view of Nakai. Claim 1, as amended, includes "... the analyzer component adjusts operation of the data clock and the transition clock according to the average operation of the set of consecutive bit times." Claim 18, as amended, includes "...adjusting data and transition clocks according to the determined average clock operation over the set of consecutive bit times." The references of record do not show, teach, or suggest the above recited limitations of claims 1 and 18. The Nakai reference fails to teach adjusting a data clock and a transition clock according to the average operation of the set of consecutive bit times. Therefore, it is not obvious to combine the teachings of Kim and Nakai to obtain the above limitations of claims 1 and 18. Claims 3 and 5-7 depend from claim 1. Claims 20-23 depend from claim 18. Therefore, claims 1, 3, 5-7, 18, and 20-23 are believed to be allowable over the references of record.

Claim 8 has been amended to include the limitations of objected claim 10. Claims 11-13 depend from claim 8. Therefore, claims 8 and 11-13 are believed to be allowable.

Claim 14 was rejected under 35 USC 103(a) as being unpatentable over Kim in view of Garlepp. Claim 14, as amended, includes "... a data clock and a transition clock, wherein the sample component employs the data clock to obtain center samples and the transition clock to obtain edge samples; and wherein the analyzer component adjusts operation of the data clock and the transition clock according to the average operation of the set of consecutive bit times." The

references of record do not show, teach, or suggest the above recited limitations of claim 14.

Claims 15-17 depend from claim 14. Therefore, claims 14-17 are believed to be allowable over

the references of record.

It is believed that the above remarks and amendments are fully responsive to the Official Action.

Reconsideration and allowance are therefore respectfully requested.

Should the Examiner have further inquiry concerning these matters, please contact the

below named attorney for Applicant.

Respectfully submitted,

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